

## In re Application of:

Leonard Forbes

Serial No.: 09/943,968

**Filed:** August 30, 2001

For: TECHNIQUE TO

SIMULTANEOUSLY DISTRIBUTE CLOCK SIGNALS AND DATA ON

INTEGRATED CIRCUITS, INTERPOSERS,

AND CIRCUIT BOARDS

Confirmation No.: 1619

Examiner: D. Ha

Group Art Unit: 2634

Attorney Docket No.: 2269-4720US

(00-1120.00/US)

**Notice of Allowance Mailed:** 

June 3, 2005

## NOTICE OF EXPRESS MAILING

Express Mail Mailing Label Number: EL994845975US

Date of Deposit with USPS: September 6, 2005

Person making Deposit: Steve Wong

## AMENDMENT PURSUANT TO 37 C.F.R. § 1.312(a)

Mail Stop ISSUE FEE Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

Sir:

Please amend the above-referenced application as follows:

Amendments to the Specification begin on page 4 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 7 of this paper.

Remarks begin on page 20 of this paper.